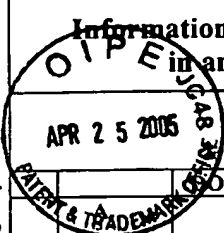


PTO-1449		Application No. 10/694,873		Applicant(s) John G. Heston			
Information Disclosure Citation in an Application 		Docket Number 004578.1377		Filing Date October 28, 2003			
U.S. PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
BS	A.	4,693,770	09/15/1987	Hatada	156	151	06/24/1986
	B.						
	C.						
	D.						
	E.						

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
							YES NO
BS	F.	DE 100 11 005 A1	July 1, 1999	Germany	H01 L 23	50	
	G.						
	H.						

NON-PATENT DOCUMENTS			
		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	I.	PCT Notification of Transmittal of The International Search Report and the Written Opinion of the International Searching Authority, or the Declaration dated 11/04/2005 for PCT/US2004/034952; 12 pages	
	J.		
	K.		
	L.		
	M.		
	N.		
	O.		
	P.		
	Q.		
	R.		
EXAMINER		DATE CONSIDERED	
BS		8/30/05	
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.			

PTO-1449	Application No. Unknown		Applicant(s) John G. Heston	
	Docket Number 004578.1377	Group Art Unit Unknown		Filing Date October 28, 2003

**Information Disclosure Citation
In an Application**

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A						
	B						
	C						
	D						
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	F						
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	K						
	L						

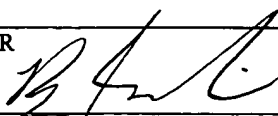
FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	M							
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	P							

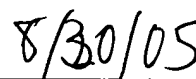
DOCUMENT (Including Author, Title, Source, Pertinent Pages, and Date)

BS	Q	Xu, et al., "A 3-10-GHz GaN-Based Flip-Chip Integrated Broad-Band Power Amplifier", IEEE Transactions on Microwave Theory and Techniques, Vol. 48, No. 12, December 2000, pp. 2573-2578.
BS	R	Debabrata Gupta, "A Novel Active Area Bumped Flip Chip Technology for Convergent Heat Transfer from Gallium Arsenide Power Devices", IEEE Transactions on Components, Packaging, and Manufacturing Technology, Part A, Vol. 18, No. 1, March 1995, pp.82-86.
	S	

EXAMINER

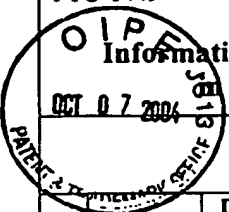


DATE CONSIDERED



EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

U.S. PATENT AND TRADEMARK OFFICE

PTO-1449  Information Disclosure Citation in an Application	Application No. 10/694,873	Applicant(s) John G. Heston	
	Docket Number 004578.1377	Group Art Unit	Filing Date October 28, 2003

U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.						
B.						
C.						
D.						
E.						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
F.								
G.								
H.								

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
I.	BS	Mamoru Kajihara and Han Park, "A systems approach delivers SiP design", http://www.eetimes.com/showArticle.jhtml?articleID=26805951 , (5 pages)	08/09/2004
J.			
K.			
L.			
M.			
N.			
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P.			
Q.			
R.			

EXAMINER

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8/30/05

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